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Threshold voltage shift and formation of charge traps induced by light irradiation during the fabrication of organic light-emitting diodes

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We examined the effects of ambient light on the device properties of an organic light-emitting diode, indium tin oxide/4,4′-bis[N-(1-naphthyl)-N-phenylamino]-biphenyl (α-NPD)/tris-(8-hydroxyquinolate) aluminum (Alq3)/Al, during fabrication using displacement current measurement. Light irradiation induces a shift in the threshold voltage for hole injection and results in the formation of charge traps in the Alq3 layer. The voltage shift implies a reduction in charge density at the α-NPD/Alq3 interface. The origin of the interfacial charge can be attributed to dipole moment ordering in the Alq3 layer. © 2008 American Institute of Physics. [DOI: 10.1063/1.2936084]

The device properties of an organic light-emitting diode (OLED) comprising 4,4′-bis[N-(1-naphthyl)-N-phenylamino]-biphenyl (α-NPD) or its derivatives and tris-(8-hydroxyquinolate) aluminum (Alq3) have been investigated by various methods including displacement current measurement (DCM).1–4 Brütting et al. revealed that hole injection from the anode to the α-NPD layer mostly occurs at biases lower than the built-in voltage (Vbi), and clarified the hole injection properties with negative charges of about −1.1 mC/cm² at the α-NPD/Alq3 interface.4 Interfacial charge is intrinsic to the α-NPD/Alq3 interface and is important for not only device operation,5 but also device degradation.5 The microscopic origin of this charge, however, remains to be clarified.

Studies have suggested the presence of a giant surface potential (GSP) across the Alq3 film to be a possible source. For example, Ito et al. reported the buildup of a GSP, during the formation of an Alq3 film on a metal substrate under dark conditions, caused by dipole moment ordering in the Alq3 film.6 The GSP grows linearly as a function of film thickness (reaching 28 V at a film thickness of 560 nm) and induces a surface charge on the film with a constant density of approximately 1.32 mC/cm².7 This value is similar to the charge density at the α-NPD/Alq3 interface. If orientation polarization of the Alq3 film persists in the OLEDs, the surface charge of the Alq3 layer should act as the charge at the α-NPD/Alq3 interface.

Interestingly, the GSP disappears on light irradiation during or after film deposition if the photon energy exceeds the absorption edge of Alq3. Two mechanisms have been proposed to remove a GSP: (i) Photoinduced formation of compensation charges and (ii) dipole moment disordering in the Alq3 film.6,8–10 Several research groups have investigated the mechanisms leading to a GSP, but little is still known about how the GSP and its decay affect the properties of OLEDs comprising Alq3 films.

In this letter, we report the effects of ambient light on the device properties of an OLED, indium tin oxide (ITO)/α-NPD/Alq3/Al, during fabrication. Light irradiation leads to the formation of charge traps and a higher threshold voltage for hole injection. The voltage shift corresponds to a decrease in the charge at the α-NPD/Alq3 interface. Considering the similarities between interfacial charge and GSP, the origin of the charge can be attributed to dipole moment ordering in the Alq3 film.

The structure of the device being studied is ITO/α-NPD (40 nm)/Alq3 (60 nm)/Al (100 nm), with a device active area of approximately 1.77 mm² [Fig. 1(a)]. The organic layers and Al electrode were successively fabricated on an ITO-coated glass substrate using a conventional vacuum sublimation technique. Two types of devices (denoted as devices A and B) were fabricated under the same conditions, except for the presence of ambient light during deposition. Throughout the deposition process, device A was fabricated under dark conditions while device B was irradiated by room light through a glass bell jar (≈1.0 µW/cm²). The devices were then transferred to a vacuum probe station. DCM and current density-voltage-luminance (J-V-L) measurements were performed in the dark at room temperature.

DCM involves capacitance-voltage measurement and was specifically introduced to analyze organic diodes.11,12 Egusa et al. conducted extensive studies to investigate the properties of model OLED interfaces.1 DCM enables us to characterize the behavior of carriers and trapped charges and that of dielectrics in organic electronic devices.4,13,14 The principle of DCM is simple: A triangular wave bias is applied to the device and the current response is measured. The experimental setup for DCM is shown in Fig. 1(a). We employed the FCE-1 measurement system for ferroelectrics.

FIG. 1. (Color online) (a) Schematic illustration of sample structure and experimental setup. (b) Schematic illustration of applied triangular wave and current response.
(Toyo Corp.). The DCM curves were measured by applying biases within ±5.0 V to the ITO layer, with reference to the Al electrode, at a sweep rate of 1 V/s. The DCM curves were measured over five sequential cycles of the triangular bias sweep [Fig. 1(b)]. The J-V-L curves were measured using a source-measure unit (model 237, Keithley) and a luminescence meter (Bm-9, Topcon).

Figure 2 shows the typical J-V-L curves of devices A and B. The conductivity and luminance of device B are lower than those of device A. This difference may be due to a combination of factors related to, for example, the properties of the interfaces, carriers, and charge traps. Although it is difficult to pinpoint the exact reason from the J-V-L curves, the DCM results at the biases below arising the actual current provide us useful information on the origin as described later.

We first explain the DCM curve of device A before discussing the effects of ambient light. Figure 3(a) shows the typical DCM curves of device A. The triangular bias sweep starts at ~4 V. Around the start voltage, the current intensity is at the base line (0.024 μA/cm²), indicating that no carrier exists in the device [Fig. 4(a)]. Subsequently, the current rises at a voltage of ~0.71 V (Vth) due to the hole injection from the ITO to the α-NPD layer [Fig. 4(b)]. We defined Vth as the inflection point of the step structure in the DCM curve. The current, again, keeps a constant intensity (0.043 μA/cm²) until the biasing voltage reaches 1.90 V (Vbi), because of hole accumulation at the α-NPD/Alq3 interface. Vth corresponds to the threshold voltage of the actual current (see inset of Fig. 2) and it agrees well with Vbi (1.9 V) of the device, estimated from the open-circuit voltage during white light irradiation. Therefore, the holes accumulated at the α-NPD/Alq3 interface can then proceed to the Alq3 layer and flow out to the Al electrode [Fig. 4(c)]. From the observed Vth and Vth, the interfacial charge density $\sigma_0$ is estimated as

$$\sigma_0 = -C_{Alq}(V_{th} - V_{inj})/S,$$

(1)

where $C_{Alq}$ is the capacitance of the Alq3 layer, and $S$ is the device active area. The equation indicates that the injected holes are captured at the α-NPD/Alq3 interface to compensate for the interfacial charge. At Vth, the amount of injected holes becomes equal to that of the interfacial charge. Finally, the electrons are injected from the Al electrode to the Alq3 layer and light is emitted [Fig. 4(d)]. The DCM curve is almost symmetrical to the $V_{th}$ axis of applied biases lower than Vth. Therefore, in the backward sweep, the reverse of processes described in Figs. 4(a)–4(d) is expected.
Figure 3(b) shows the typical DCM curves of device B as a function of applied triangular bias with the number of sweep cycles. The DCM curve of the second sweep, of device B shifted slightly from that of the first sweep, if a bias higher than $V_{th}$ was applied. We confirmed that the DCM curves traced the same path as that of the second sweep while applying further bias sweeps, and conclude that the device was in a steady state after the sweeps.

The effect of ambient light during device fabrication is evident from the DCM curves. In the steady state, $V_{th}$ estimated from the DCM curve of device B is significantly larger than that of device A, although the $V_{th}$ values are similar [Fig. 3(c) and inset of Fig. 2]. These results are attributed to the difference in $\sigma_0$ ($\approx V_{th} - V_{inj}$) of devices A and B. Using Eq. (1), the $\sigma_0$ of device A is obtained from the DCM curves as $-1.13 \pm 0.03$ mC/m$^2$ (10 samples), while that of devices B is $-0.56 \pm 0.13$ mC/m$^2$ (13 samples). The $\sigma_0$ of device A is very stable and in good agreement with that reported previously.

Here it should be noted that ambient light was controlled during the film deposition process. After device fabrication, both devices were incidentally or intentionally exposed to light; however, further changes in $\sigma_0$ were not observed. From these results, we conclude that $\sigma_0$ is reduced by light irradiation during device fabrication.

We now report similarities between the charge at the $\alpha$-NPD/Alq$_3$ interface and the GSP of the Alq$_3$ film. As described earlier, the surface charge density induced by the GSP is approximately 1.32 mC/m$^2$ and is independent of film thickness. Similar values have been reported for different substrates by other groups, e.g., 1.25 mC/m$^2$ for Au substrate and 0.80–1.33 mC/m$^2$ for Al substrates. The observed $\sigma_0$ of device A is comparable to these values and independent of Alq$_3$ film thickness.

In addition, both $\sigma_0$ and the GSP are reduced by light irradiation during film deposition. These results suggest that the charge at the $\alpha$-NPD/Alq$_3$ interface and the GSP could have the same origin, i.e., dipole moment ordering in the Alq$_3$ film.

In our devices, $\sigma_0$ did not change even if the devices were exposed to white light (LEDs or halogen lamp, $\sim 10$ mW/cm$^2$) after fabrication. In contrast, the GSP decay results from light irradiation even after film deposition. The difference in the decay mechanism between the GSP and interfacial charge could be a reason for this. The GSP was examined on the free surface of the Alq$_3$ film deposited on a metal or glass substrate without applying an external biasing voltage. Thus, when carriers are formed in the film by light irradiation, they are captured at the film surface because of surface charges. Consequently, the GSP disappears even if dipole moment ordering persists in the film. In the device, however, $\sigma_0$ is unlikely to be reduced by the compensation charges, because the charges can be removed (except in the strongly trapped case) by applying an external biasing voltage. Thus, the interfacial charges in the device remain as long as the dipole moment in the film is ordered.

In Fig. 3(b), the DCM curve obtained by applying a bias lower than $V_{th}$ is shown, indicating a steady state before hole injection into the Alq$_3$ layer. This curve and that obtained by the first forward sweep for a higher bias ($\geq V_{th}$) coincide with each other [inset of Fig. 3(b)], indicating a slight shift of $V_{inj}$ and $V_{th}$ caused by hole injection into the Alq$_3$ layer. This shift suggests that part of the holes injected into the Alq$_3$ layer were trapped during the first forward sweep [Fig. 4(e)], and holes caused variations in the electric field distribution [Fig. 4(f)]. Because $V_{inj}$ and $V_{th}$ shift almost equally, the first sweep is less than $V_{th}$ (1.9 V), there should be an attracting electric field to trap the holes in the Alq$_3$ layer [Fig. 4(e)]. On the other hand, device A was mostly trap-free. It seems that negative trapped charges, formed due to light irradiation during device fabrication, contributed to such an electric field in the device.

Kondakov et al. reported that $\sigma_0$ decreased with device degradation and device efficiency. Our findings show that light irradiation during device fabrication caused a similar effect even in fresh devices and implies that decreasing the orientation polarization in the Alq$_3$ layer may be responsible for device degradation. This scenario is consistent with observations of relatively lower conductivity and luminescence in device B (Fig. 2).

In summary, we found that light irradiation during device fabrication reduces the charge density at the $\alpha$-NPD/Alq$_3$ interface. The relationship between interfacial charge density and the GSP of an Alq$_3$ film was discussed. The results suggest that the charge at the $\alpha$-NPD/Alq$_3$ interface is due to dipole moment ordering in the Alq$_3$ film. We also found that light irradiation causes charge traps in the Alq$_3$ layer. It should be noted that most OLED materials have a permanent dipole moment because the lower symmetry in molecular structure is designed to suppress crystallization and form amorphous films. A similar effect may occur in devices composed of materials possessing a permanent dipole moment. Also, ambient light should be considered for device fabrication.

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7. In Ref. 6, surface charge density was determined to be $0.44$ mC/m$^2$. It was derived by subtracting electronic polarization from orientation polarization. The latter is necessary for our discussion. Throughout this letter, we assumed the dielectric constant of Alq$_3$ to be 3.0.